(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization International Bureau





(43) International Publication Date 17 July 2003 (17.07.2003)

PCT

(10) International Publication Number WO 03/058833 A1

(51) International Patent Classification:

10.

- H04B 1/40
- (21) International Application Number: PCT/IB02/05349
- (22) International Filing Date: 9 December 2002 (09.12.2002)
- (25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data: 02075026.1

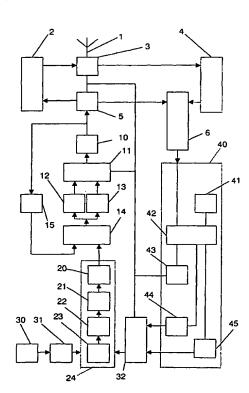
7 January 2002 (07.01.2002) EP

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- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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(54) Title: TRANSCEIVER WITH MULTI-STATE DIRECT DIGITAL SYNTHESIZER DRIVEN PHASE LOCKED LOOP



(57) Abstract: Transceivers for use in time division telecommunication units like mobile phones and base stations can be produced at lower costs by, in a transmitting mode, switching the direct digital synthesizer (DDS 24) driven phase locked loop (PLL 10-15) into a modulating state and supplying a modulation signal to the DDS and switching in the PLL a first filter (12) allowing the generation of an improved modulated signal, and by, in a receiving mode, switching the DDS driven PLL into an oscillating state and supplying a non-modulation signal to the DDS and switching in the PLL a second filter (13) allowing demodulation with reduced phase noise. A transmitter part (2,6) share a single DDS driven PLL, based upon the basic idea of using important parts in low cost transceivers for both modes, instead of using different parts for different modes, and achieve a good performance.

WO 03/058833 A1



Published:

- with international search report
- before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments

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INTERNATIONAL SEARCH REPORT

PCT/IB-02/05349

A. CLASSIFICATION OF SUBJECT MATTER IPC 7 H04B1/40
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols) IPC 7 H04B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

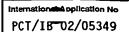
WPI Data, PAJ, EPO-Internal

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χ	US 5 423 076 A (MROCH ALAN B ET AL) 6 June 1995 (1995-06-06)	1-3,6-14
Α	0 04110 2000 (2000 00 00)	4
	column 4, line 12 - line 15 column 5, line 5 -column 6, line 6 column 6, line 36 - line 50 column 9, line 6 - line 34 column 9, line 53 - line 58 column 10, line 4 -column 11, line 2 figures 1,3	
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Y Patent family members are listed in annex.
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Date of mailing of the International search report 08/05/2003
Authorized officer Masche, C

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Category *	Citation of document, with indication, where appropriate, of the relevant passages		Retevant to claim No.
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Internation pplication No PCT/IB-U2/05349

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